SEARCH REQUEST FORM Scientific and Technical Information Center

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Requester's Full 1	Name: Brag Lon	Reginald 1	Examiner# :	Date: _	05/22/02
Art Unit:	Phone Number:	30	Serial Number:(39/82(896	r ρ
Mail Box and Blo	lg/Room Location:	Re	esults Format Prefer	red (circle): Paper	Disk E-mail
	ne search is submitted,				******
species or structures,	iled statement of the search topic, keywords, synonyms, acronyms, a a special meaning. Give examples abstract.	and registry numbers, and	combine with the conce	pt or utility of the inventi	on. Define any
Title of Invention					
Inventors (please	provide full names):				
Earliest Priority F	iling Date:				· · · · · · · · · · · · · · · · · · ·
For Sequence Searc appropriate serial nu	hes Only Please include all perti mber	inent information (parent,	child, divisional, or iss	ued patent numbers) alon	g with the
From: Sent: To: Subject:	Bragdon, Reginald Wednesday, May 22, 2002 1:37 PM STIC-EIC2100 Litigation Search				
	gation search for reissue 0 when the search is ready a				
Reg					
Reginald G. Braga Primary Examiner Art Unit 2186 TC 2100 SPRE Deta 703-305-3823		·			
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STAFF USE ON	71	Type of search	Vend	lors and cost where a	pplicable
Searcher: / rer	-17611	NA Sequence (#)	STN _		
Searcher Phone: 30	18 -183/	AA Sequence (#)	Dialog	_	
Searcher Location:	<u>- 4840</u>	Structure (#)	Questo	eVOrbit M/N	
Date Searcher Picked Up	-/'-/	Bibliographic	Dr. Li	1	
Date Completed:	55/23/02	Litigation	Lexis/	Nexis) n m	
Searcher Prep & Review	Time: <u>6</u>	Full Text	Seque	nce System	
Clerical Prep Time:	1	Patent Family	www	//Internet	
Online Time:	1.)	Other	Other	(specify)	

Source: All Sources > Area of Law - By Topic > Patent Law > Patents > U.S. Patents > Utility, Design and Plant Patents

Terms: patno=6145023 (Edit Search)

Pat. No. 6145023, *

LEXIS-NEXIS **Library: PATENT** ALL File:

6,145,023

◆ GET 1st DRAWING SHEET OF 4

Nov. 7, 2000

Information storage and information processing system utilizing state-designating member provided on supporting card surface which produces write-permitting or write-inhibiting signal

REISSUE: Reissue Application filed Apr. 6, 2001 (O.G. Jul. 17, 2001) Ex. Gp.: 2181; Re. S.N.

09/826,896

INVENTOR: Iwasaki, Hiroshi, Yokohama, Japan

ASSIGNEE-AT-ISSUE: Kabushiki Kaisha Toshiba, Kawasaki, Japan (03)

APPL-NO: 592,508

FILED: Jan. 26, 1996

CERTCORR: Jan. 1, 2002 a Certificate of Correction was issued for this patent (O.G. Jan. 22,

2002)

FOR-PRIOR:

Feb. 3, 1995 Japan 7-017185

INT-CL: [7] G06F 13#12; G11B 23#03

US-CL: 710#13; 710#8; 340#870.16; 361#684; 361#737; 455#41; 360#133

SEARCH-FLD: 340#870.16; 361#684, 737; 235#492; 455#41; 360#133; 710#13, 8

PRIM-EXMR: An, Meng-Ai T.

ASST-EXMR: Perveen, Rehana

LEGAL-REP: Finnegan, Henderson, Farabow, Garrett & Dunner, L.L.P.

CORE TERMS: conductive, storage, memory, seal, medium, information storage, module, recess, state-designating, external...

Source: All Sources > Area of Law - By Topic > Patent Law > Patents > U.S. Patents > Utility, Design and Plant

Patents 1

Terms: patno=6145023 (Edit Search)

View: Custom - Modify

Segments: After-issue, Appl-no, Assign-action, Assign-contact, Assign-date, Assign-frame, Assign-reel, Assignee,

Assignor-info, Asst-exmr, At-issue, Certcorr, Date, Exmr, Expiration-date, Filed, For-prior, Govt-int, Int-cl, Inventor, Legal-rep, Lit-reex, Parcase, Patno, Pct-filed, Pct-no, Pct-pub-date, Pct-pub-no, Prim-exmr, Reex-cert,

Reissue, Rel-us-data, Search-fld, Sect-102e-date, Sect-371-date, Title, Us-cl

Date/Time: Thursday, May 23, 2002 - 12:01 PM EDT

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No Documents Found

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LEXIS-NEXIS
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File: CASES

5/23/02 12:02 PM

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LEXIS-NEXIS
Library: PATENT
File: JNLS

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?us6145023/pn
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 Search statement
?prt full nonstop legalall
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 PN - US6145023 A 20001107 [US6145023]
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       state-designating member provided on supporting card surface which
       produces write-permitting or write-inhibiting signal
     - (A) TOKYO SHIBAURA ELECTRIC CO (JP)
     - (A) IWASAKI HIROSHI
                            (JP)
     - US59250896 19960126
                            [1996US-0592508]
     - JP1718595 19950203 [1995JP-0017185]
     - (A) G06F-013/12 G11B-023/03
     - G11C-007/24
     - G11C-008/20
 PCL - ORIGINAL (0): 710013000; CROSS-REFERENCE (X): 340870160 360133000
       361684000 361737000 455041000 710008000
     - Corresponding document
 CT - US4264917; US4703420; US4837628; US4860127; US4860142; US4882702;
       US4916662; US4943464; US4980856; US5018017; US5036429; US5153818;
       US5172338; US5184282; US5210671; US5226006; US5272374; US5293236;
       US5297029; US5297148; US5299089; US5343319; US5375037; US5388084;
       US5430859; US5438359; US5457590; US5469399; US5475441; US5488433;
       US5508971; US5509018; US5535328; US5539600; US5550709; US5559993;
       US5563825; US5566105; US5568424; US5572466; US5572478; US5584043;
       US5592420; US5596532; US5602987; US5608673; US5611057; US5615344;
       US5621685; US5638321; US5663901; US5671229; US5693570; US5887145;
       EP0214478; EP0588517 A1; EP0708429 A1; DE4322666 A1; JP64-55691;
       JP4-16396; GB818554; GB2208029
     - European Patent Office Publication, Patent Abstracts of Japan,
       Publication No. 63137387, dated Jun. 9, 1988.
 STG - (A) United States patent
 AB - In a small and thin memory module for sharing data among electronic
       devices such as information processing apparatuses, a write prohibit
       state can be visually recognized. A conductive seal is attached to a
       predetermined position on a support member, thereby setting the memory
       module in the write prohibit state. The conductive seal visually
       indicates the write prohibit state. When the memory module is mounted
       in a connector section of a card-shaped holder, connector pins are
       electrically connected to each other via the conductive seal. Thus, a
       write prohibit mechanism is realized at low cost.
 UP - 2000-44
 1/1 LGST - (C) LEGSTAT
 PN - US 6145023 [US6145023]
 AP - US 592508/96 19960126 [1996US-0592508]
 DT - US-P
 ACT - 19960126 US/AE-A
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     - 20001107 US/A
       PATENT
     - 20010717 US/RF
       REISSUE APPLICATION FILED
       20010406
     - 20020101 US/CC
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CERTIFICATE OF CORRECTION

UP - 2002-06

1/1 CRXX - (C) CLAIMS/RRX

PN - 6,145,023 A 20001107 [US6145023] PA - Toshiba Corp JP

ACT - 20010406 REISSUE REQUESTED

ISSUE DATE OF O.G.: 20010717

REISSUE REQUEST NUMBER: 09/826896

EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2181

Reissue Patent Number:

- 20020122 CERTIFICATE OF CORRECTION

1/2 PAST - (C) Thomson Derwent

AN - 200204-000143

PN - 6145023 A [US6145023]

OG - 2002-01-22

ACT - CERTIFICATE OF CORRECTION

2/2 PAST - (C) Thomson Derwent

AN - 200129-001180

PN - 6145023 A [US6145023]

OG - 2001-07-17

ACT - REISSUE APPLICATION FILED

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       DATENVERARBEITUNGSANORDNUNG
 IN
     - IWASAKI HIROSHI [JP]
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     - TOSHIBA KAWASAKI KK [JP]
     - DE 69614726/96-A 19960201 [1996DE-6014726]
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 IC - G11C-007/00; G06K-019/073
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 DT - DE-P
 ACTE- 20011004 DE/REF-P
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 UP - 2001-40
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     FR: TRANSLATION NOT FILED
UP - 2002-17
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PN - EP 1113447 A1 20010704 [EP1113447]
TI - INFORMATION STORAGE APPARATUS AND INFORMATION PROCESSING METHOD USING
     THE SAME AND CONNECTOR PIN ASSEMBLY
LA - ENG
IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - EP 1104401/01-A 19960201 [2001EP-1104401]
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IC - G11C-007/00; G06K-019/073; G11C-007/16
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UP ~ 2002-13
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IC - G11C-007/00; G06K-019/073
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   - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
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    USING THE SAME
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IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - EP 96101411/96-A 19960201 [1996EP-0101411]
PR - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G11C-007/00
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PN - JP 8212017 A2 19960820 [JP08212017]
TI - INFORMATION STORAGE DEVICE AND PROCESSOR USING SAME
IN - IWASAKI HIROSHI
PA - TOKYO SHIBAURA ELECTRIC CO
AP - JP 17185/95-A 19950203 [1995JP-0017185]
PR - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G06F-003/06
7/14 INPADOC - (C) INPADOC
PN - KR 232940 B1 20000115 [KR-232940]
TI - INFORMATION STORAGE APPARATUS AND INFORMATION PROCESSING APPARATUS
     USING THE SAME
IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - KR 9602507/96-A 19960202 [1996KR-0002507]
PR - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G11B-020/04
8/14 INPADOC - (C) INPADOC
PN - SG 35490 A1 19970201 [SG--35490]
TI - INFORMATION STORAGE APPARATUS AND INFORMATION PROCESSING APPARATUS
     USING THE SAME
IN - IWASAKI HIROSHI
PA - TOKYO SHIBAURA ELECTRIC CO
AP - SG 9600583/96-A 19960129 [1996SG-0000583]
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PR - SG 9600583/96-A 19960129 [1996SG-0000583]
    - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G09G-003/00; G11C-007/00
9/14 INPADOC - (C) INPADOC
PN - TW 440822 B 20010616 [TW-440822]
TI - INFORMATION MEMORIZATION APPARATUS AND INFORMATION PROCESSING
     APPARATUS USING THIS MEMORIZATION APPARATUS
IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - TW 85100823/96-A 19960124 [1996TW-0100823]
PR - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G11B-023/28
10/14 INPADOC - (C) INPADOC
PN - US 6094697 A 20000725 [US6094697]
TI - INFORMATION PROCESSING SYSTEM INCLUDING PROCESSING DEVICE FOR
     DETECTING NON-CONDUCTIVITY OF STATE-INDICATING NON-CONDUCTIVE MEMBER
     AND DISCRIMINATING PROHIBIT STATE OF WRITING INFORMATION TO
     INFORMATION WRITABLE STORAGE MEDIUM
IN - IWASAKI HIROSHI [JP]
PA - KABUHIKI KAISHA TOSHIBA [JP]
AP - US 143739/98-A 19980828 [1998US-0143739]
PR - US 143739/98-A 19980828 [1998US-0143739]
    - JP 17185/95-A 19950203 [1995JP-0017185]
   - US 592508/96-A1 19960126 [1996US-0592508]
IC - G06F-012/14; G06F-012/16
1/1 LEGALI - (C) LEGSTAT
PN - US 6094697 [US6094697]
AP - US 143739/98 19980828 [1998US-0143739]
DT - US-P
ACTE- 19980828 US/AE-A
     APPLICATION DATA (PATENT)
      {US 143739/98 19980828 [1998US-0143739]}
    - 20000725 US/A
     PATENT
UP - 2000-34
11/14 INPADOC - (C) INPADOC
PN - US 6145023 A 20001107 [US6145023]
TI - INFORMATION STORAGE AND INFORMATION PROCESSING SYSTEM UTILIZING
     STATE-DESIGNATING MEMBER PROVIDED ON SUPPORTING CARD SURFACE WHICH
     PRODUCES WRITE-PERMITTING OR WRITE-INHIBITING SIGNAL
IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - US 592508/96-A 19960126 [1996US-0592508]
PR - JP 17185/95-A 19950203 [1995JP-0017185]
IC - G06F-013/12; G11B-023/03
1/1 LEGALI - (C) LEGSTAT
PN - US 6145023 [US6145023]
AP - US 592508/96 19960126 [1996US-0592508]
DT - US-P
ACTE- 19960126 US/AE-A
     APPLICATION DATA (PATENT)
      {US 592508/96 19960126 [1996US-0592508]}
    - 20001107 US/A
     PATENT
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- 20010717 US/RF
      REISSUE APPLICATION FILED
      20010406
     20020101 US/CC
      CERTIFICATE OF CORRECTION
UP - 2002-06
12/14 INPADOC - (C) INPADOC
PN - US 2001005291 AA 20010628 [US20010005291]
   - INFORMATION STORAGE APPARATUS AND INFORMATION PROCESSING APPARATUS
      USING THE SAME
   - IWASAKI HIROSHI [JP]
IN
   - TOKYO SHIBAURA ELECTRIC CO [US]
AP - US 769314/01-A 20010126 [2001US-0769314]
PR - US 769314/01-A 20010126 [2001US-0769314]
    - JP 17185/95-A 19950203 [1995JP-0017185]
    - US 658189/00-A3 20000908 [2000US-0658189]
- US 372678/99-A3 19990812 [1999US-0372678]
    - US 143839/98-A3 19980831 [1998US-0143839]
- US 592508/96-A3 19960126 [1996US-0592508]
IC - G11B-005/027; G11B-023/033; G11B-023/087
1/1 LEGALI - (C) LEGSTAT
PN - US 2001005291 [US20010005291]
   - US 769314/01 20010126 [2001US-0769314]
DT - US-P
ACTE- 20010126 US/AE-A
      APPLICATION DATA (PATENT)
      {US 769314/01 20010126 [2001US-0769314]}
    - 20010628 US/A1A1
      PATENT APPLICATION PUBLICATION (PRE-GRANT)
UP - 2001-29
13/14 INPADOC - (C) INPADOC
PN - US 6292850 BA 20010918 [US6292850]
   - INFORMATION STORAGE SYSTEM INCLUDING STATE-DESIGNATING AREA ON MEMORY
      CARD AND DETECTING PRESENCE OR ABSENCE OF STATE-DESIGNATING MEMBER ON
      STATE-DESIGNATING AREA TO INHIBIT OR ALLOW WRITING OF INFORMATION
IN - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [US]
AP - US 372678/99-A 19990812 [1999US-0372678]
   - US 372678/99-A 19990812 [1999US-0372678]
    - JP 17185/95-A 19950203 [1995JP-0017185]
    - US 143739/98-A3 19980828 [1998US-0143739]
    - US 592508/96-A3 19960126 [1996US-0592508]
IC - G06F-013/14; G06F-013/38
1/1 LEGALI - (C) LEGSTAT
PN - US 6292850 [US6292850]
AP - US 372678/99 19990812 [1999US-0372678]
DT - US-P
ACTE- 19990812 US/AE-A
      APPLICATION DATA (PATENT)
      {US 372678/99 19990812 [1999US-0372678]}
    - 20010918 US/BA
      PATENT (NO PREVIOUS PRE-GRANT PUBLICATION)
UP - 2001-40
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PN - US 6338104 BA 20020108 [US6338104]
TI - SYSTEM INCLUDING SINGLE CONNECTOR PIN SUPPORTER HAVING TWO SEPARATE
       PLURALITY OF CONNECTOR PINS WITH ONE SET OF PINS CONTACTING STATE
      DESIGNATING PORTION OF MEMORY CARD INDICATING WRITE PROHIBIT STATE
IN
    - IWASAKI HIROSHI [JP]
PA - TOKYO SHIBAURA ELECTRIC CO [JP]
AP - US 658189/00-A 20000908 [2000US-0658189]
PR - US 658189/00-A 20000908 [2000US-0658189]
    - JP 17185/95-A 19950203 [1995JP-0017185]
    - US 372678/99-A3 19990812 [1999US-0372678]

- US 143739/98-A3 19980828 [1998US-0143739]

- US 592508/96-A3 19960126 [1996US-0592508]
IC - G06F-013/14; G06F-013/16
1/1 LEGALI - (C) LEGSTAT
PN - US 6338104 [US6338104]
    - US 658189/00 20000908 [2000US-0658189]
DT - US-P
ACTE- 20000908 US/AE-A
      APPLICATION DATA (PATENT)
       {US 658189/00 20000908 [2000US-0658189]}
    - 20020108 US/BA
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PATENT (NO PREVIOUS PRE-GRANT PUBLICATION)

UP - 2002-05